H-PSOF8L 9.90x10.38x2.30, 1.20P CASE 100DC **ISSUE B** DATE 30 APR 2024 6.64 -(2x) 🛆 ccc в D 0.80 (2X) 1.20 D2 (2x) TERMINAL 1 CORNER $\overline{\mathbf{X}}$ Α ♨ [⊥]⊖1 XXX E2¹(2x)-2.20 DETAIL "A" 10.20 -e/2 6.80 Ŧ SCALE: 2X h1 F (DATUM A) ר|(4X) ⊖ Α. E2 10b (8x) 🖞 ← A 2 63 7 95 2.70 bbb M C A B D4 (2x) ¢ ddd M C LAND PATTERN RECOMMENDATION /6\ - L2 (8x) -L1 💦 FOR ADDITIONAL INFORMATION ON OUR PB-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DETAIL "B" DOWNLOAD THE ONSEMI SOLDERING AND MOUNTING TOP VIEW SCALE: 2X TECHNIQUES REFERENCE MANUAL. SOLDERRM/D. DETAIL "B" NOTES: 1. PACKAGE STANDARD REFERENCE: JEDEC MO-299, ISSUE B. 2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2018. 3. "e" REPRESENTS THE TERMINAL PITCH. // aaa C 4. THIS DIMENSION INCLUDES ENCAPSULATION THICKNESS "A1", A1 AND PACKAGE BODY THICKNESS, BUT DOES NOT INCLUDE ATTACHED FEATURES, e.g., EXTERNAL OR CHIP CAPACITORS. AN INTEGRAL HEATSLUG IS NOT CONSIDERED AS ATTACHED FEATURE. C SIDE VIEW 5. A VISUAL INDEX FEATURE MUST BE LOCATED WITHIN THE HATCHED AREA 6. DIMENSIONS b1,L1,L2 APPLY TO PLATED TERMINALS. D1 \Box ccc (2x) 7. THE LOCATION AND SIZE OF EJECTOR MARKS ARE OPTIONAL. 8. THE LOCATION AND NUMBER OF FUSED LEADS ARE OPTIONAL. D5 (2x) D6 D3 (2x) MILLIMETERS MILLIMETERS (2x) DIM DIM MIN. NOM. MAX. MIN. NOM. MAX. L3 2.20 2.30 2.40 E5 9.36 9.47 Α 9.46 A1 1.70 1.80 1.90 E6 1.10 1.20 1.30 0.70 0.80 0.90 F7 0.15 0.18 0.21 b E6 b1 9.70 9.80 9.90 1.20 BSC е (DATUM A) (3x) 0.35 0.45 0.55 0.60 BSC b2 e/2 E1 E3 E4 E5 0.40 0.50 0.60 Н 11.58 11.68 11.78 с D 10.28 10.38 10.48 5.74 5.84 5.94 H/2 b2 (8x) D/2 5.09 5.19 5.29 H1 .15 BSC D1 10.98 11.08 11.18 L 1.63 1.73 1.83 D2 3.20 3.30 3.40 L1 0.60 0.70 0.80 /8\ D3 2.70 2.80 L2 0.60 0.70 2.60 0.50 HEAT SLUG TERMINAL D/2 D4 4.45 4.55 4.65 L3 0.43 0.53 0.63 L (8x) D5 3.20 3.30 3.40 θ 10° REF H/2 (DATUM B)-D6 0.55 0.65 0.75 θ1 10° REF GENERIC Е 9.80 9.90 10.00 aaa 0.20 H1 **MARKING DIAGRAM*** E1 7.30 7.40 7.50 bbb 0.25 BOTTOM VIEW E2 0.30 0.40 0.50 0.20 ccc AYWWZZ E3 7.40 7.50 7.60 ddd 0.20 E4 8.20 8.30 8.40 eee 0.10 XXXX = Specific Device Code *This information is generic. Please refer to A = Assembly Location device data sheet for actual part marking. Υ = Year Pb-Free indicator, "G" or microdot "•", may XXXXXXXX = Work Week WW or may not be present. Some products may XXXXXXXX not follow the Generic Marking. = Assembly Lot Code 77 Electronic versions are uncontrolled except when accessed directly from the Document Repository. **DOCUMENT NUMBER:** 98AON80466G Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red. **DESCRIPTION:** H-PSOF8L 9.90x10.38x2.30, 1.20P PAGE 1 OF 1 onsemi and ONSEMI are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves

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