

Hotplate and Double Pulse Generator Evaluation Board User's Manual

EVBUM2901G-EVB

Hotplate and Double Pulse Generator Description

A Hotplate and Double pulse generator extension board is designed to provide HOT temperature testing conditions and variable 10pulse PWM generation for a Discrete double pulse tester.

The Extension board together with the Discrete Double pulse tester supports HOT temperature testing of all onsemi's discrete packages (SiC, Si) within 1200 V breakdown voltage using Daughter cards.

The purpose of the Extension board is integrating laboratory PWM generator, heat source and +5 V output into a compact solution powered from a single 12 V adapter (Included).

This document provides a user' manual for using the Extension board together with discrete Double pulse tester. Details of layout, schematics and bill of materials are included in the user's manual.

Evaluation Board Operation

The board is designed as RoHS compliant. Design of the board was not qualified for manufacturing. No tests were made on whole operating temperature range. No lifetime tests were performed. The board must be used in laboratory environment only and must be operated by skilled personal trained on all safety standards. Further details of used components are in their respective datasheets. PWM resolution and value of sensed temperature are indicative.

Features

- Hotplate PCB with Low Thermal Resistance
- Hotplate Temperature Regulation (OFF, 125°C, 150°C, 175°C)
- 10pulse PWM Generator (5 V, Freq. 30 kHz, Pulse Width from 0.2 μ s to 10 μ s)
- Fully Compatible with Discrete Double Pulse Tester
- Power Adapter Included
- Intuitive Interface Using LED Indicators and Display

Discrete double pulse tester user's manual: EVBUM2897

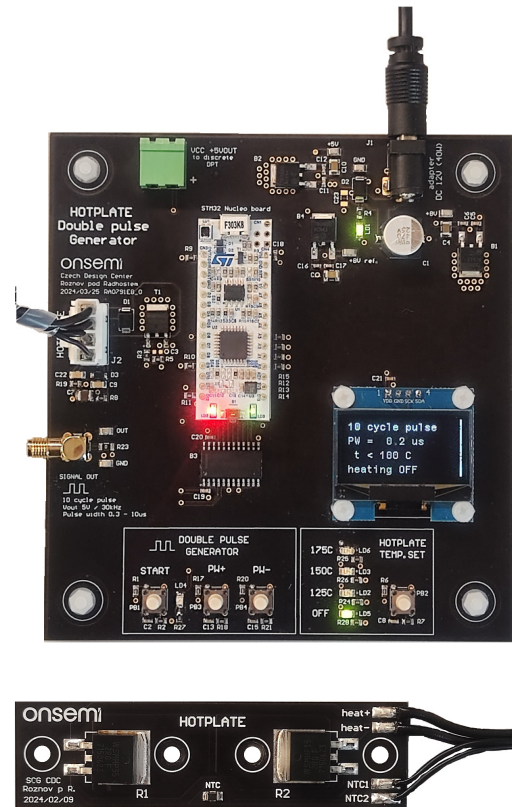


Figure 1. Hotplate and Double Pulse Signal Generator Board

APPLICATIONS INFORMATION

Controlling Board

The control board is powered from AC/DC 12 V 40 VA adapter (included). The power supply part contains a linear LDO source +8 V, +8 Vref. for powering the NUCLEO F303K8 control processor. Power Source +5 V 1 A is intended for external discrete DPT power supply (+VCC connector).

The Controlling board can be used simultaneously for generating a PWM signal – SMA connector signal OUT and controlling heat for hotplate module (connector HOTPLATE).

Generator control and hotplate control are separated. The buttons can be used to select functions (START generation, Pulse width length, temperature selection) LEDs indicate the set hotplate temperature.

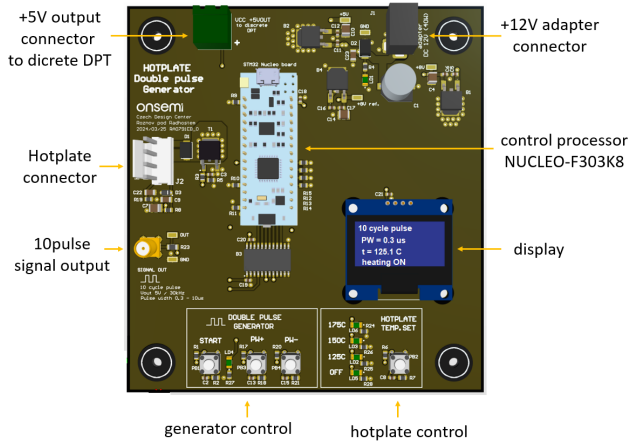


Figure 2. Controlling Board

HOTPLATE Board

Hotplate PCB is a 1-layer IMS PCB adapted for mounting to discrete daughter cards. The PCB is assembled with two heating resistors R1, R2 in series connection. The NTC thermistor senses actual temperature on the hotplate. Thermal conducting pad is included. For better heat distribution on the DUT transistors, it is recommended to stick it on the HOTPLATE module.

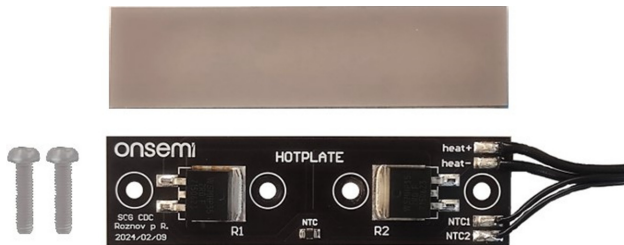


Figure 3. Hotplate Board, Thermal Conducting Pad

Temperature Regulation

The temperature is regulated by a regulation loop using Anti-WindUp Proportional-Sumation Controller. The output of this controller is a variable duty cycle (generated using 30 kHz switching frequency), which controls heating resistors on the Hotplate. Power switching of the heating resistors is ensured by onsemi's 47 mΩ N-Channel Logic Level Power MOSFET, controlled directly from +5 V/0 V logic. Temperature sensing is ensured using 4.7 kΩ NTC on a Hotplate. Measured temperature range is limited by 200°C upper limit and 100°C lower limit (non-linear properties of NTC). The regulation loop schematic is shown on Figure 4.

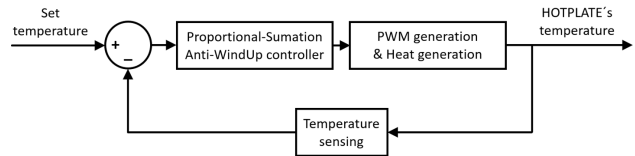


Figure 4. Regulation Loop Block Schematic

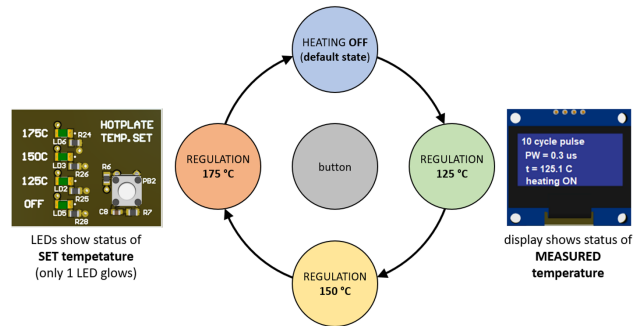


Figure 5. Describes User's Controlling of the Hotplate

There is only single button which switches between 4 states when pressed.

Double Pulse Generating

10pulse generation includes 3 seconds delay before the generation and also 2 seconds delay after the generation. This is implemented because of safety and user's comfort. START button is disabled when 10pulse generation (including delay) is in progress.

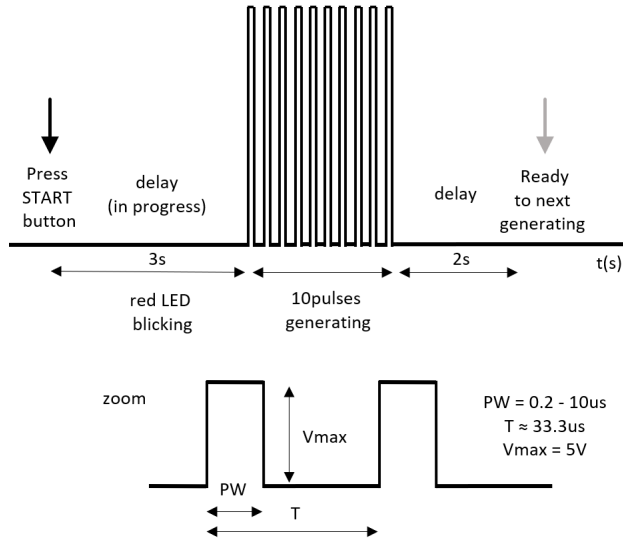
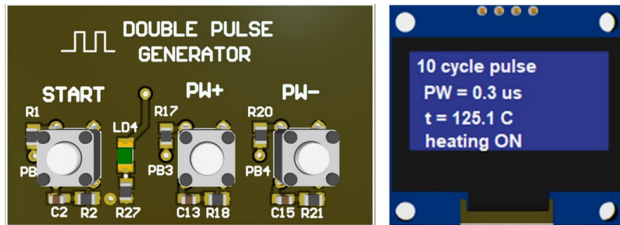


Figure 6. Timing Waveform Double Pulse Generating



button START	starts 10pulse generation (including delay)	
button PW+	increases set value of PW (Pulse Width)	
button PW-	decreases set value of PW (Pulse Width)	
LED LD4	blank	10pulse generation (including delay) ready
	blinking	10pulse generation (including delay) in progress

Figure 7. Describes User's Controlling of the 10pulse Generator

Protective Functions

10pulse generation feature of the board can be simply excluded by user's controlling or disconnecting signal output. Heat generation feature using Hotplate can be also simply excluded by user's controlling or disconnecting Hotplate from a connector. Software in microcontroller includes feature for detection of Hotplate's disconnection. Figure 8 shows Hotplate's disconnected status shown on a display. Status of set temperature is set to default off state when every disconnection is detected.

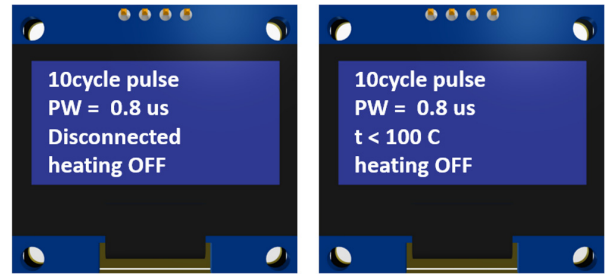


Figure 8. HOTPLATE Connector Disconnected and Connected Display Status

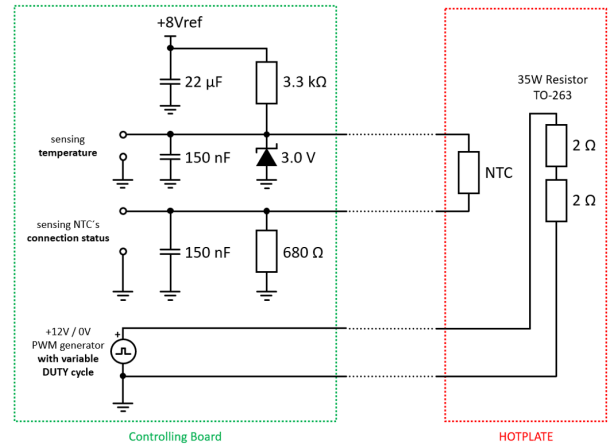


Figure 9. Hotplate Board Connection Schematic

Measurement Process

Turning on procedure Discrete DPT together with Hotplate.

- Ensure that all power sources are turned off
- Ensure that setup is fully prepared (check connection of a High Voltage power source, used gate resistors, used Daughter cards, using thermal conducting pad between Hotplate and tested devices, probes connection according to Figure 12 etc.)
- Plug 230 VAC/12 VDC Adapter into controlling board, then into 230 VAC Grid
- Make sure that all power supply indication LEDs glow
- Verify that high side and low side gate to source voltages are equal to V_{EE} (V_{EE} defined by used insulated DC/DC)
- Do a testing 10pulse generation using START button and check corresponding between measured Gate to Source signal and information showed on a display
- Make sure that Pulse Width is set to 0.2 μs
- Turn on High voltage power supply
- Do a Double Pulse Testing with respect to limits of tested devices

ATTENTION: Touch only user buttons when using with the powered controlling board, connecting or disconnecting hotplate from its connector must be done before powering the controlling board.

CAUTION: Do not touch HOTPLATE board and daughter card during operation. The temperature reaches up to 175°C. Risk of burns! Wait a few minutes after turning off the heating.

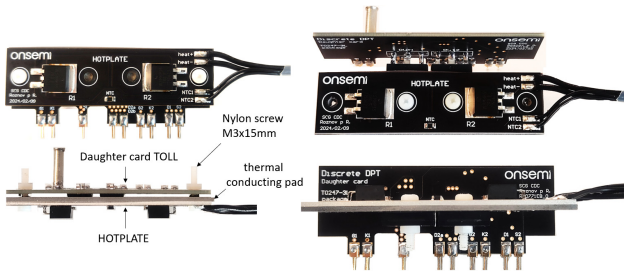


Figure 10. Mounting Hotplate on the Daughter Card TOLL and TO247-4L Package

Recommended mounting to the daughter card is using set high temperature nylon screws M3x15 mm (included)
Recommended mounting torque $\approx 0.05 - 0.2$ Nm

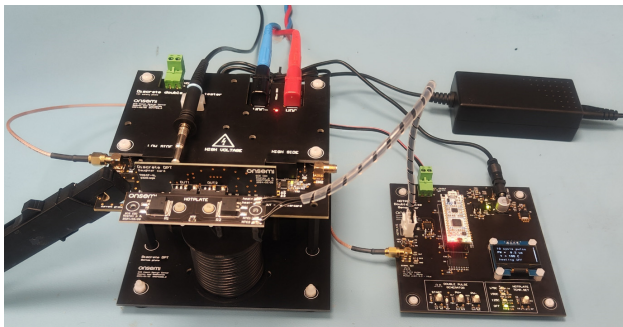


Figure 11. Measurement Setup – Discrete DPT + Extension Board Hotplate and Double Pulse Signal Generator

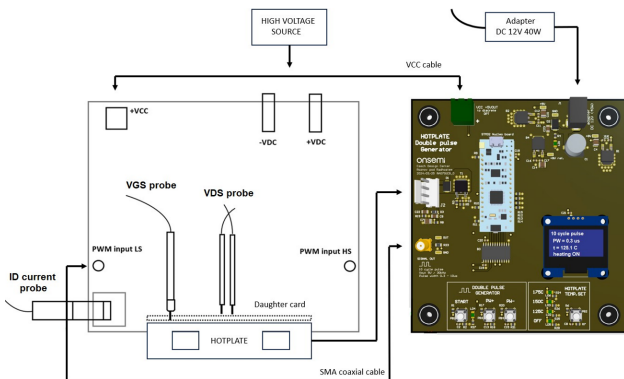


Figure 12. Double Pulse Test Setup for HOT Temperature

Application Test

Figure X shows thermal camera measurements for 150°C set temperatures. The measurements show that NTC's temperature corresponds to a set temperature. Temperature at the tested device's package is very similar to a set temperature, because of a designing NTC into the middle of the hotplate and designing heating resistors as close as possible to the tested device's package. The purpose of the heat generation using the hotplate is providing very similar testing conditions for a testing device with very similar package.

Figure 15 shows Double pulse test waveforms V_{DS} , I_D , V_{GS} in hot temperature 150°C. Measurement setup was used according to Figure 11 and 12. The waveforms do not show any large oscillations during switching.

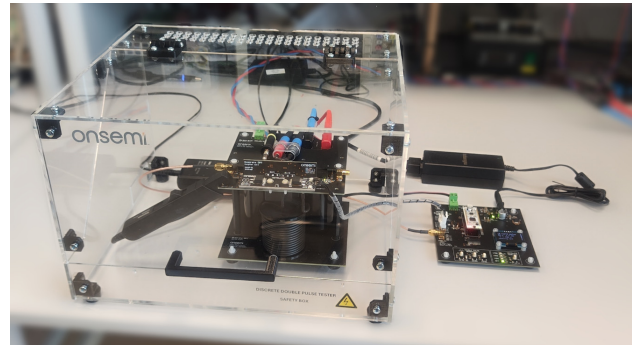


Figure 13. onsemi Safety Box for Double Pulse Testing

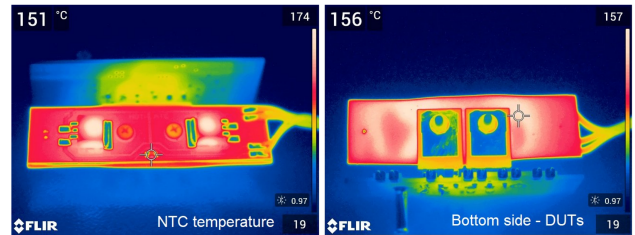


Figure 14. Thermal Camera Measurements

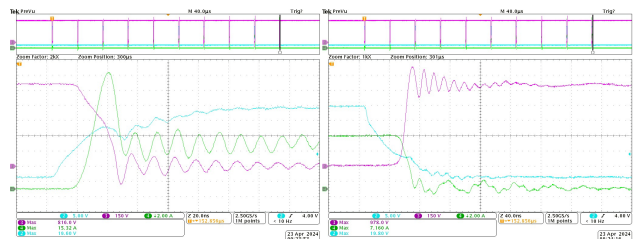


Figure 15. Double Pulse Test Waveforms in Hot Temperature TO247-4L NTH4L022N120M3S

Waveforms: blue – V_{GS} , green – I_D , red – V_{DS}

DPT conditions: $V_{DC} = 800$ V, $I_D \approx 70$ A, $R_G = 3R9$, $T_j = 150^\circ\text{C}$, $V_{GS} = 18/-3$ V, $PW = 1.4$ μs

Controlling Board Schematic

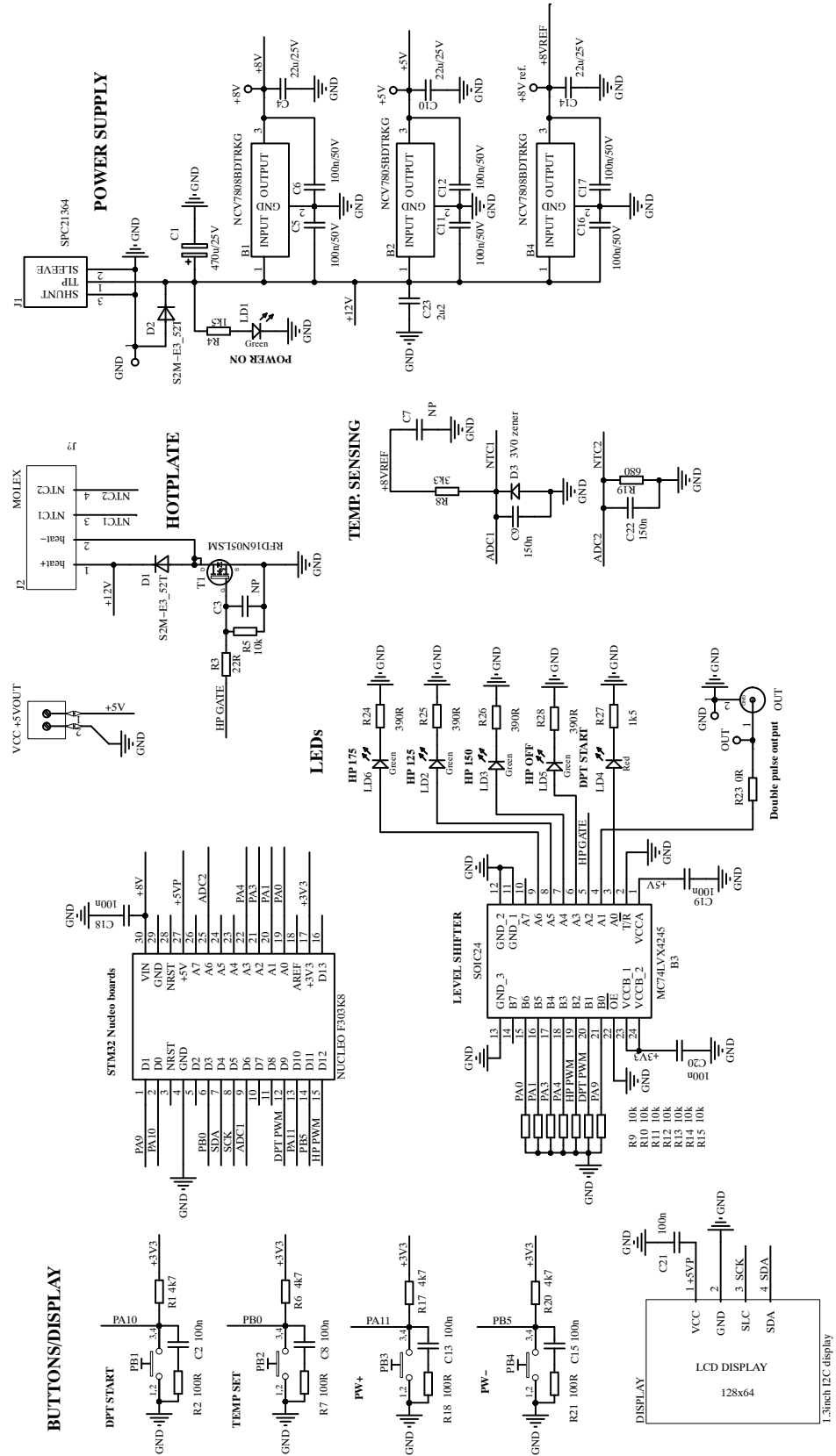


Figure 16. Controlling Board Schematic

onsemi, **onsemi**, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "**onsemi**" or its affiliates and/or subsidiaries in the United States and/or other countries. **onsemi** owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of **onsemi**'s product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

The evaluation board/kit (research and development board/kit) (hereinafter the "board") is not a finished product and is not available for sale to consumers. The board is only intended for research, development, demonstration and evaluation purposes and will only be used in laboratory/development areas by persons with an engineering/technical training and familiar with the risks associated with handling electrical/mechanical components, systems and subsystems. This person assumes full responsibility/liability for proper and safe handling. Any other use, resale or redistribution for any other purpose is strictly prohibited.

THE BOARD IS PROVIDED BY ONSEMI TO YOU "AS IS" AND WITHOUT ANY REPRESENTATIONS OR WARRANTIES WHATSOEVER. WITHOUT LIMITING THE FOREGOING, ONSEMI (AND ITS LICENSORS/SUPPLIERS) HEREBY DISCLAIMS ANY AND ALL REPRESENTATIONS AND WARRANTIES IN RELATION TO THE BOARD, ANY MODIFICATIONS, OR THIS AGREEMENT, WHETHER EXPRESS, IMPLIED, STATUTORY OR OTHERWISE, INCLUDING WITHOUT LIMITATION ANY AND ALL REPRESENTATIONS AND WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, TITLE, NON-INFRINGEMENT, AND THOSE ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE CUSTOM OR TRADE PRACTICE.

onsemi reserves the right to make changes without further notice to any board.

You are responsible for determining whether the board will be suitable for your intended use or application or will achieve your intended results. Prior to using or distributing any systems that have been evaluated, designed or tested using the board, you agree to test and validate your design to confirm the functionality for your application. Any technical, applications or design information or advice, quality characterization, reliability data or other services provided by **onsemi** shall not constitute any representation or warranty by **onsemi**, and no additional obligations or liabilities shall arise from **onsemi** having provided such information or services.

onsemi products including the boards are not designed, intended, or authorized for use in life support systems, or any FDA Class 3 medical devices or medical devices with a similar or equivalent classification in a foreign jurisdiction, or any devices intended for implantation in the human body. You agree to indemnify, defend and hold harmless **onsemi**, its directors, officers, employees, representatives, agents, subsidiaries, affiliates, distributors, and assigns, against any and all liabilities, losses, costs, damages, judgments, and expenses, arising out of any claim, demand, investigation, lawsuit, regulatory action or cause of action arising out of or associated with any unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of any products and/or the board.

This evaluation board/kit does not fall within the scope of the European Union directives regarding electromagnetic compatibility, restricted substances (RoHS), recycling (WEEE), FCC, CE or UL, and may not meet the technical requirements of these or other related directives.

FCC WARNING – This evaluation board/kit is intended for use for engineering development, demonstration, or evaluation purposes only and is not considered by **onsemi** to be a finished end product fit for general consumer use. It may generate, use, or radiate radio frequency energy and has not been tested for compliance with the limits of computing devices pursuant to part 15 of FCC rules, which are designed to provide reasonable protection against radio frequency interference. Operation of this equipment may cause interference with radio communications, in which case the user shall be responsible, at its expense, to take whatever measures may be required to correct this interference.

onsemi does not convey any license under its patent rights nor the rights of others.

LIMITATIONS OF LIABILITY: **onsemi** shall not be liable for any special, consequential, incidental, indirect or punitive damages, including, but not limited to the costs of requalification, delay, loss of profits or goodwill, arising out of or in connection with the board, even if **onsemi** is advised of the possibility of such damages. In no event shall **onsemi**'s aggregate liability from any obligation arising out of or in connection with the board, under any theory of liability, exceed the purchase price paid for the board, if any.

The board is provided to you subject to the license and other terms per **onsemi**'s standard terms and conditions of sale. For more information and documentation, please visit www.onsemi.com.

ADDITIONAL INFORMATION

TECHNICAL PUBLICATIONS:

Technical Library: www.onsemi.com/design/resources/technical-documentation
onsemi Website: www.onsemi.com

ONLINE SUPPORT: www.onsemi.com/support

For additional information, please contact your local Sales Representative at www.onsemi.com/support/sales