

Electrical Rules Check Report

Class	Document	Message
		Successful Compile for OBC_PFC_power_board.PrjPcb

Design Rules Verification Report

Filename : C:\Users\fg4ngv\AppData\Local\Temp\Snapshot\1\design_files\OBC_PFC_power_board.PcbDoc

Warnings 0
Rule Violations 0

Warnings	
Total	0

Rule Violations	
Clearance Constraint (Gap=0.203mm) (All),(IsPad and (Name Like 'Free-FIX?'))	0
Clearance Constraint (Gap=0.203mm) (All),(All)	0
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint (All)	0
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Width Constraint (Min=0.254mm) (Max =20mm) (Preferred=1.27mm) (All)	0
Routing Layers(All)	0
Routing Via (Templates Used To Check Via: v24h12_NCP51705, v102h51, v165h81, v250h178, v350h178) (All)	0
Differential Pairs Uncoupled Length using the Gap Constraints (Min=0.254mm) (Max =0.254mm) (Prefered=0.254mm)	0
Power Plane Connect Rule(Relief Connect)(Expansion=0.508mm) (Conductor Width=0.254mm) (Air Gap=0.254mm)	0
Fabrication Testpoint Style (Under Component=Yes) (All)	0
Fabrication Testpoint Usage (Valid =Don't care, Allow multiple per net=No) (All)	0
Assembly Testpoint Style (Under Component=Yes) (All)	0
Assembly Testpoint Usage (Valid =Don't care, Allow multiple per net=No) (All)	0
Hole Size Constraint (Min=0.254mm) (Max =5.5mm) (All)	0
Pads and Vias to follow the Drill pairs settings	0
Hole To Hole Clearance (Gap=0.254mm) (Not ((IsPad) And (Name Like 'LCMC-?'))),(Not ((IsPad) And (Name Like	0
Minimum Solder Mask Sliver (Gap=0.1mm) (All),(All)	0
Silk To Solder Mask (Clearance=0.203mm) (IsPad),(All)	0
Silk to Silk (Clearance=0.254mm) (All),(All)	0
Net Antennae (Tolerance=0mm) (All)	0
Component Clearance Constraint (Horizontal Gap = 0mm, Vertical Gap = 0mm) (IsComponent And ((Name Like	0
Component Clearance Constraint (Horizontal Gap = 0mm, Vertical Gap = 0mm) (IsComponent And (Name Like	0
Component Clearance Constraint (Horizontal Gap = 0mm, Vertical Gap = 0mm) (IsComponent And ((Name Like	0
Component Clearance Constraint (Horizontal Gap = 0mm, Vertical Gap = 0mm) (InComponent('FAN')),(IsComponent	0
Component Clearance Constraint (Horizontal Gap = 0mm, Vertical Gap = 0mm) (IsComponent And (Name Like	0
Component Clearance Constraint (Horizontal Gap = 0mm, Vertical Gap = 0mm) (IsComponent And (Name Like	0
Component Clearance Constraint (Horizontal Gap = 0mm, Vertical Gap = 0mm) (IsComponent And ((Name Like	0
Component Clearance Constraint (Horizontal Gap = 0mm, Vertical Gap = 0mm) (IsComponent And ((Name Like	0
Component Clearance Constraint (Horizontal Gap = 0mm, Vertical Gap = 0mm) (IsComponent And ((Name Like	0
Component Clearance Constraint (Horizontal Gap = 0mm, Vertical Gap = 0mm) (InComponent('GR1')),(IsComponent	0
Component Clearance Constraint (Horizontal Gap = 0mm, Vertical Gap = 0mm) (IsComponent And (Name =	0
Component Clearance Constraint (Horizontal Gap = 0mm, Vertical Gap = 0mm)	0
Component Clearance Constraint (Horizontal Gap = 0mm, Vertical Gap = 0mm)	0
Component Clearance Constraint (Horizontal Gap = 0mm, Vertical Gap = 0mm)	0
Component Clearance Constraint (Horizontal Gap = 0mm, Vertical Gap = 0mm)	0
Component Clearance Constraint (Horizontal Gap = 0.254mm, Vertical Gap = 0.254mm) (All),(All)	0
Component Clearance Constraint (Horizontal Gap = 0mm, Vertical Gap = 0mm) (InComponent('HS_A')),(IsComponent	0
Component Clearance Constraint (Horizontal Gap = 0mm, Vertical Gap = 0mm)	0
Component Clearance Constraint (Horizontal Gap = 0mm, Vertical Gap = 0mm)	0
Component Clearance Constraint (Horizontal Gap = 0mm, Vertical Gap = 0mm) (InComponent('HS_B')),(IsComponent	0
Component Clearance Constraint (Horizontal Gap = 0mm, Vertical Gap = 0mm) (InComponent('HS_C')),(IsComponent	0
Height Constraint (Min=0mm) (Max =25.4mm) (Prefered=12.7mm) (Disabled)(All)	0
Silk primitive without silk layer	0
Total	0