# Electrical Rules Check Report

| Class | Document | Message |
|---|---|---|
| | | Successful Compile for OBC_LLC_switch_board.PrjPcb |
| | | |

# Design Rules Verification Report

Filename : C:\Users\fg4ngv\AppData\Local\TempReleases\Snapshot\1\design_files\OBC_

Warnings 0
Rule Violations 0

| Warnings | |
|---|---|
| Total | 0 |

| Rule Violations | |
|---|---|
| Clearance Constraint (Gap=8mil) (All),(All) | 0 |
| Clearance Constraint (Gap=59.055mil) (InNet('PGND')),(InNet('P700V') Or InNet('P700V_PSU') Or | 0 |
| Clearance Constraint (Gap=59.055mil) (InNet('P700V') Or InNet('P700V_PSU')),(InNet('PGND') Or | 0 |
| Clearance Constraint (Gap=59.055mil) (InNet('GND')),(InNet('NetCO1H_neg') Or InNet('NetCO1H_pos')) | 0 |
| Clearance Constraint (Gap=8mil) (InNet('GND')),(InComponent('J_XIO')) | 0 |
| Clearance Constraint (Gap=157.48mil) (InNet('P700V') Or InNet('P700V_PSU') Or InNet('PGND')),((ObjectKind = 'Pad') | 0 |
| Short-Circuit Constraint (Allowed=No) (All),(All) | 0 |
| Un-Routed Net Constraint ( (All) ) | 0 |
| Modified Polygon (Allow modified: No), (Allow shelved: No) | 0 |
| Width Constraint (Min=8mil) (Max=300mil) (Preferred=30mil) (All) | 0 |
| Power Plane Connect Rule(Direct Connect )(Expansion=20mil) (Conductor Width=10mil) (Air Gap=10mil) (Entries=4) | 0 |
| Hole Size Constraint (Min=11.811mil) (Max=236.22mil) (All) | 0 |
| Pads and Vias to follow the Drill pairs settings | 0 |
| Hole To Hole Clearance (Gap=10mil) (All),(All) | 0 |
| Minimum Solder Mask Sliver (Gap=4mil) (All),(All) | 0 |
| Silk To Solder Mask (Clearance=8mil) (IsPad),(All) | 0 |
| Silk to Silk (Clearance=4mil) (All),(All) | 0 |
| Net Antennae (Tolerance=0mil) (All) | 0 |
| Component Clearance Constraint ( Horizontal Gap = 0mil, Vertical Gap = 0mil ) (IsComponent And ((Name = 'J_SWB') | 0 |
| Component Clearance Constraint ( Horizontal Gap = 10mil, Vertical Gap = 10mil ) (All),(All) | 0 |
| Component Clearance Constraint ( Horizontal Gap = 0mil, Vertical Gap = 0mil ) (IsComponent And ((Name = 'J_SWA') | 0 |
| Component Clearance Constraint ( Horizontal Gap = 0mil, Vertical Gap = 0mil ) (IsComponent And ((Name Like 'QB?') | 0 |
| Component Clearance Constraint ( Horizontal Gap = 0mil, Vertical Gap = 0mil ) (IsComponent And ((Name Like 'QA?') | 0 |
| Component Clearance Constraint ( Horizontal Gap = 0mil, Vertical Gap = 0mil ) (IsComponent And ((Name Like 'DB?') | 0 |
| Component Clearance Constraint ( Horizontal Gap = 0mil, Vertical Gap = 0mil ) (IsComponent And ((Name Like 'DA?') | 0 |
| Component Clearance Constraint ( Horizontal Gap = 0mil, Vertical Gap = 0mil ) (IsComponent And (Name Like | 0 |
| Component Clearance Constraint ( Horizontal Gap = 0mil, Vertical Gap = 0mil ) (IsComponent And (Name = | 0 |
| Component Clearance Constraint ( Horizontal Gap = 0mil, Vertical Gap = 0mil ) (IsComponent And (Name = | 0 |
| Component Clearance Constraint ( Horizontal Gap = 0mil, Vertical Gap = 0mil ) (IsComponent And (Name = | 0 |
| Component Clearance Constraint ( Horizontal Gap = 0mil, Vertical Gap = 0mil ) (IsComponent And (Name = | 0 |
| Component Clearance Constraint ( Horizontal Gap = 0mil, Vertical Gap = 0mil ) (IsComponent And ((Name = 'Q2') Or | 0 |
| Component Clearance Constraint ( Horizontal Gap = 0mil, Vertical Gap = 0mil ) (IsComponent And ((Name = 'Q1') Or | 0 |
| Component Clearance Constraint ( Horizontal Gap = 0mil, Vertical Gap = 0mil ) (IsComponent And ((Name = 'Q4') Or | 0 |
| Component Clearance Constraint ( Horizontal Gap = 0mil, Vertical Gap = 0mil ) (IsComponent And ((Name = 'Q5') Or | 0 |
| Component Clearance Constraint ( Horizontal Gap = 0mil, Vertical Gap = 0mil ) (IsComponent And (Name = | 0 |
| Component Clearance Constraint ( Horizontal Gap = 0mil, Vertical Gap = 0mil ) (IsComponent And (Name = | 0 |
| Component Clearance Constraint ( Horizontal Gap = 0mil, Vertical Gap = 0mil ) (IsComponent And ((Name Like | 0 |
| Component Clearance Constraint ( Horizontal Gap = 0mil, Vertical Gap = 0mil ) (IsComponent And ((Name = 'U8') Or | 0 |
| Component Clearance Constraint ( Horizontal Gap = 0mil, Vertical Gap = 0mil ) (IsComponent And ((Name = 'CP1') Or | 0 |
| Component Clearance Constraint ( Horizontal Gap = 0mil, Vertical Gap = 0mil ) (IsComponent And (Name = | 0 |
| Component Clearance Constraint ( Horizontal Gap = 0mil, Vertical Gap = 0mil ) (IsComponent And (Name = | 0 |
| Component Clearance Constraint ( Horizontal Gap = 0mil, Vertical Gap = 0mil ) (IsComponent And (Name = | 0 |
| Component Clearance Constraint ( Horizontal Gap = 0mil, Vertical Gap = 0mil ) (IsComponent And (Name = | 0 |
| Component Clearance Constraint ( Horizontal Gap = 0mil, Vertical Gap = 0mil ) (IsComponent And ((Name = 'J_SEC2') | 0 |
| Component Clearance Constraint ( Horizontal Gap = 0mil, Vertical Gap = 0mil ) (IsComponent And ((Name = 'J_SEC1') | 0 |
| Component Clearance Constraint ( Horizontal Gap = 0mil, Vertical Gap = 0mil ) (IsComponent And ((Name = | 0 |
| Component Clearance Constraint ( Horizontal Gap = 0mil, Vertical Gap = 0mil ) (IsComponent And ((Name = | 0 |
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| Component Clearance Constraint ( Horizontal Gap = 0mil, Vertical Gap = 0mil ) (IsComponent And (Name = | 0 |
| Component Clearance Constraint ( Horizontal Gap = 0mil, Vertical Gap = 0mil ) (IsComponent And (Name = | 0 |
| Component Clearance Constraint ( Horizontal Gap = 0mil, Vertical Gap = 0mil ) (IsComponent And (Name = | 0 |
| Component Clearance Constraint ( Horizontal Gap = 0mil, Vertical Gap = 0mil ) (IsComponent And (Name = | 0 |
| Height Constraint (Min=0mil) (Max=2362.205mil) (Prefered=500mil) (IsComponent And OnTop) | 0 |
| Height Constraint (Min=0mil) (Max=1181.102mil) (Prefered=500mil) (IsComponent And OnBottom) | 0 |

| Rule Violations | |
|---|---|
| Silk primitive without silk layer | 0 |
| Total | 0 |