

# Electrical Rules Check Report

Class	Document	Message
		Successful Compile for OBC_LLC_resonant_tank_WE_board.PrjPcb

# Design Rules Verification Report

Filename : C:\Users\fg4ngv\AppData\Local\TempReleases\Snapshot\1\design\_files\OBC\_

Warnings 0  
Rule Violations 0

Warnings	
Total	0

Rule Violations	
Clearance Constraint (Gap=0.203mm) (All),(All)	0
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint ( All )	0
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Width Constraint (Min=0.203mm) (Max=13mm) (Preferred=0.762mm) (All)	0
Routing Layers(All)	0
Routing Via (MinHoleWidth=0.3mm) (MaxHoleWidth=1.5mm) (PreferredHoleWidth=0.762mm) (MinWidth=0.762mm)	0
Power Plane Connect Rule(Relief Connect )(Expansion=0.508mm) (Conductor Width=0.254mm) (Air Gap=0.254mm)	0
Hole Size Constraint (Min=0.3mm) (Max=10mm) (All)	0
Pads and Vias to follow the Drill pairs settings	0
Hole To Hole Clearance (Gap=0.254mm) (All),(All)	0
Minimum Solder Mask Sliver (Gap=0.102mm) (All),(All)	0
Silk To Solder Mask (Clearance=0.203mm) (IsPad),(All)	0
Silk to Silk (Clearance=0.102mm) (All),(All)	0
Net Antennae (Tolerance=0mm) (All)	0
Board Clearance Constraint (Gap=0mm) (All)	0
Component Clearance Constraint ( Horizontal Gap = 0mm, Vertical Gap = 0mm ) (IsComponent And ((Name = 'J_SWA')	0
Component Clearance Constraint ( Horizontal Gap = 0mm, Vertical Gap = 0mm )	0
Component Clearance Constraint ( Horizontal Gap = 0mm, Vertical Gap = 0mm )	0
Component Clearance Constraint ( Horizontal Gap = 0mm, Vertical Gap = 0mm ) (IsComponent And ((Name = 'J_S2')	0
Component Clearance Constraint ( Horizontal Gap = 0mm, Vertical Gap = 0mm ) (IsComponent And ((Name = 'J_SWB')	0
Component Clearance Constraint ( Horizontal Gap = 0mm, Vertical Gap = 0mm ) (IsComponent And ((Name = 'J_S1')	0
Component Clearance Constraint ( Horizontal Gap = 0mm, Vertical Gap = 0mm ) (InComponent('BRL')),(IsComponent	0
Component Clearance Constraint ( Horizontal Gap = 0mm, Vertical Gap = 0mm ) (InComponent('BRR')),(IsComponent	0
Component Clearance Constraint ( Horizontal Gap = 0mm, Vertical Gap = 0mm )	0
Component Clearance Constraint ( Horizontal Gap = 0mm, Vertical Gap = 0mm )	0
Component Clearance Constraint ( Horizontal Gap = 0.254mm, Vertical Gap = 0.254mm ) (All),(All)	0
Component Clearance Constraint ( Horizontal Gap = 0mm, Vertical Gap = 0mm ) (InComponent('FAN1')),(IsComponent	0
Component Clearance Constraint ( Horizontal Gap = 0mm, Vertical Gap = 0mm )	0
Height Constraint (Min=0mm) (Max=65mm) (Preferred=12.7mm) (All)	0
Silk primitive without silk layer	0
Total	0