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FCD5N60 N 沟道 SuperFET[®] MOSFET 600 V, 4.6 A, 950 mΩ

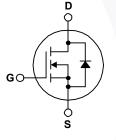
特性

- 650 V @ T_J = 150°C
- Typ.R_{DS(on)} = 810 mΩ
- 超低栅极电荷 (典型值 Q_g = 16 nC)
- 低有效输出电容 (典型值 C_{oss(eff.)}= 32 pF)
- 100% 经过雪崩测试
- 符合 RoHS 标准

应用

- LCD/LED 电视和显示器
- 照明
- 光伏逆变器
- AC-DC 电源





SuperFET[®] MOSFET 是飞兆半导体第一代利用电荷平衡技术实现出色低导通电阻和更低栅极电荷性能的高压超级结(SJ)

MOSFET 系列产品。这项技术专用于最小化导通损耗并提供卓

越的开关性能、dv/dt 额定值和更高雪崩能量。因此,SuperFET

MOSFET 非常适合开关电源应用,如功率因数校正 (PFC)、服务器/电信电源、平板电视电源、ATX 电源及工业电源应用。

MOSFET 最大额定值 Tc=25°C 除非另有说明。

符号		参数		FCD5N60TM / FCD5N60TM_WS	单位
V _{DSS}	漏极一源极电压			600	V
	记行中诉	- 连续 (T _C =25°C)		4.6	Α
D	漏极电流	- 连续 (T _C =100°C)		2.9	A
DM	漏极电流	- 脉冲	(说明1)	13.8	А
V _{GSS}	栅极一源极电压			±30	V
E _{AS}	单脉冲雪崩能量		(说明2)	159	mJ
AR	雪崩电流		(说明1)	4.6	Α
E _{AR}	重复雪崩能量		(说明1)	5.4	mJ
dv/dt	二极管恢复 dv/dt 峰值		(说明 3)	4.5	V/ns
D		(T _C = 25°C)		54	W
P _D	功耗	- 降低至 25°C 以上		0.43	W/°C
T _J , T _{STG}	工作和存储温度范围			-55 至 +150	°C
ΤL	用于焊接的最大引线温度,	距离外壳 1/8",持续 5 秒		300	°C

说明

热性能

符号	参数	FCD5N60TM / FCD5N60TM_WS	单位
$R_{\theta JC}$	结至外壳热阻最大值	2.3	°C/W
R_{\thetaJA}	结至环境热阻最大值	83	0/11

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封装标识与定购信息

器件编号	顶标	封装	包装方法	卷尺寸	带宽	数量
FCD5N60TM	FCD5N60	D-PAK	卷带	330 mm	16 mm	2500 个
FCD5N60TM_WS	FCD5N60	D-PAK	卷带	330 mm	16 mm	2500 个

电气特性 T_C =25°C 除非另有说明。

符号 参数	测试条件	最小值	典型值	最大值	单位
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关断特性

P\/	BV _{DSS} 漏极一源极击穿电压	V _{GS} = 0 V, I _D = 250 μA, T _C = 25°C	600	-	-	V
DVDSS		V_{GS} = 0 V, I _D = 250 µA, T _C = 150°C	-	650	-	V
ΔΒV _{DSS} / ΔΤ _J	击穿电压温度系数	I _D = 250 μA,参考温度为 25°C	-	0.6	-	V/°C
BV _{DS}	漏源极雪崩击穿电压	V _{GS} = 0 V, I _D = 4.6 A	-	700	-	V
1	零栅极电压漏极电流	V _{DS} = 600 V, V _{GS} = 0 V	-	-	1	μA
IDSS	令 伽似电压	V _{DS} = 480 V, T _C = 125°C	-	-	10	μΑ
I _{GSS}	栅极 - 体漏电流	V_{GS} = ±30 V, V_{DS} = 0 V	-	-	±100	nA

导通特性

V _{GS(th)}	栅极阈值电压	$V_{GS} = V_{DS}, I_{D} = 250 \ \mu A$	3.0	-	5.0	V
R _{DS(on)}	漏极至源极静态导通电阻	V_{GS} = 10 V, I _D = 2.3 A	-	0.81	0.95	Ω
9 _{FS}	正向跨导	$V_{\rm DS}$ = 40 V, I _D = 2.3 A	-	3.8	_	S

动态特性

C _{iss}	输入电容		-	470	600	pF
C _{oss}	输出电容	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	-	250	320	pF
C _{rss}	反向传输电容		-	22	-	pF
C _{oss}	输出电容	V _{DS} = 480 V, V _{GS} = 0 V, f = 1 MHz	-	12	-	pF
C _{oss(eff.)}	有效输出电容	V _{DS} = 0 V 至 400 V, V _{GS} = 0 V	-	32	-	pF

开关特性

t _{d(on)}	导通延迟时间			-	12	30	ns
t _r	开通上升时间	$V_{DD} = 300 \text{ V}, \text{ I}_{D} = 4.6 \text{ A},$		-	40	90	ns
t _{d(off)}	关断延迟时间	V _{GS} = 10 V, R _G = 25 Ω		-	47	95	ns
t _f	关断下降时间		(说明4)	-	22	55	ns
Q _{g(tot)}	10 V 的栅极电荷总量	V _{DS} = 480 V, I _D = 4.6 A,		-	16	-	nC
Q _{gs}	栅极 - 源极栅极电荷	V _{GS} = 10 V		-	2.8	-	nC
Q _{gd}	栅极 - 漏极 " 米勒 " 电荷		(说明4)	-	7	-	nC

漏极 - 源极二极管特性

I _S	漏极 - 源极二极管最大正向连续电流		-		4.6	Α
I _{SM}	漏极 - 源极二极管最大正向脉冲电流		-	-	13.8	Α
V _{SD}	漏极 - 源极二极管正向电压	V _{GS} = 0 V, I _{SD} = 4.6 A	-	-	1.4	V
t _{rr}	反向恢复时间	V _{GS} = 0 V, I _{SD} = 4.6 A	-	295	-	ns
Q _{rr}	反向恢复电荷	$V_{GS} = 0 V, I_{SD} = 4.6 A$ $dI_F/dt = 100 A/\mu s$	-	2.7		μC

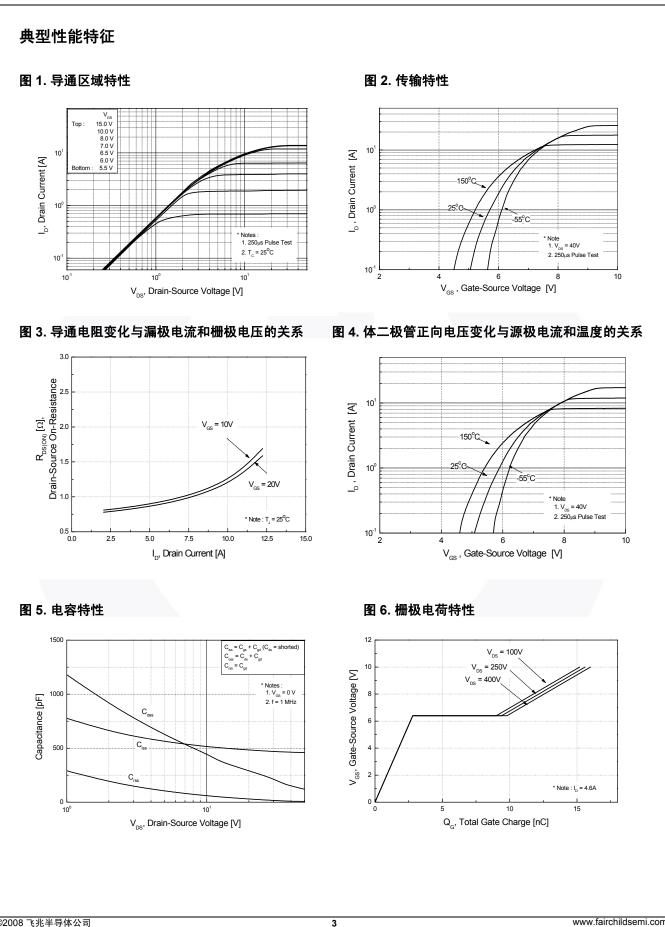
注意:

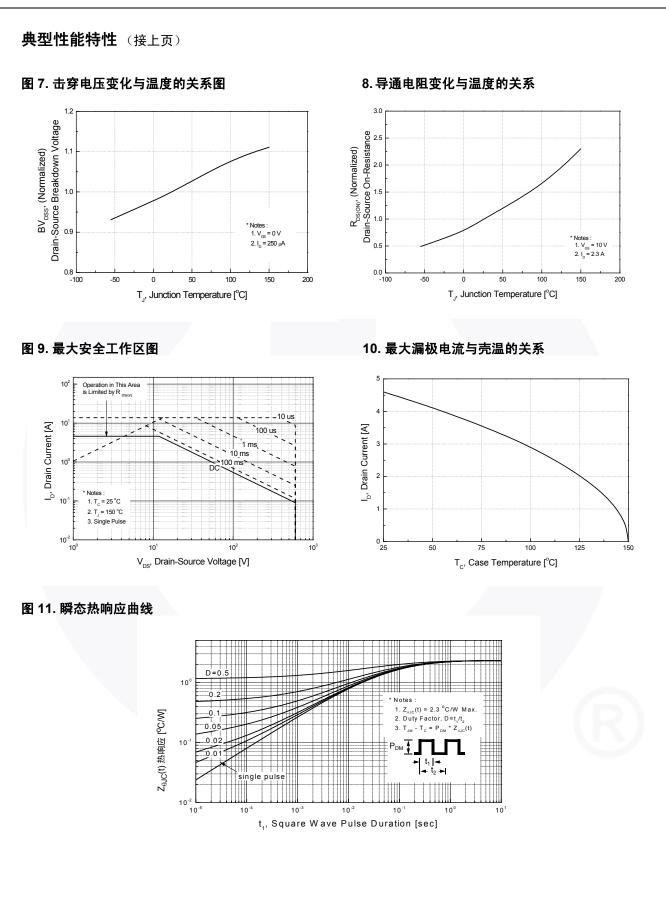
1. 重复额定值:脉冲宽度受限于最大结温。

2. I_{AS} =2.3 A, V_{DD} =50 V, R_{G} =25 Ω, 开始 T_{J} =25°C。

3. $I_{SD} \leq 4.6 \text{ A}$, di/dt $\leq 200 \text{ A}/\mu s$, $V_{DD} \leq BV_{DSS}$, \mathcal{H} t $_J$ =25°C .

4. 本质上独立于工作温度的典型特性。



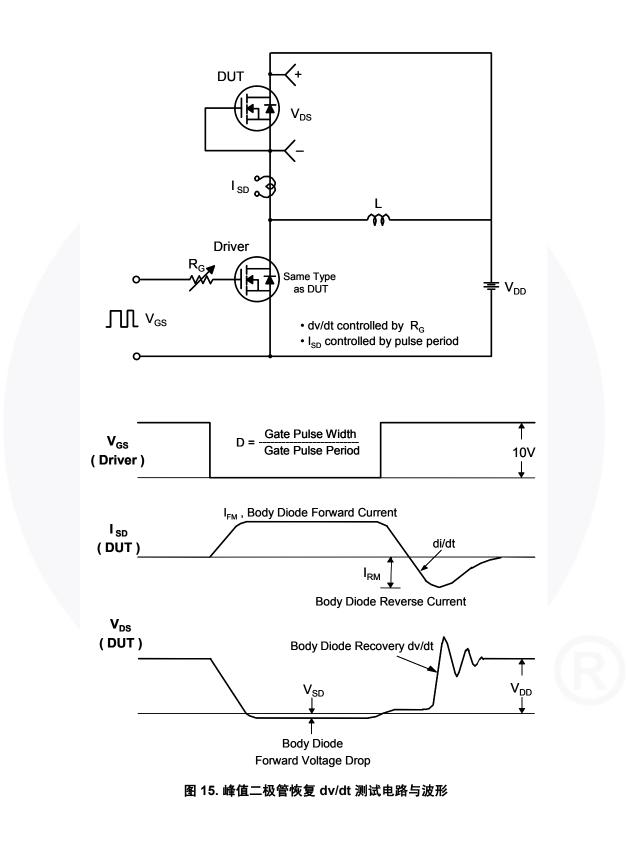


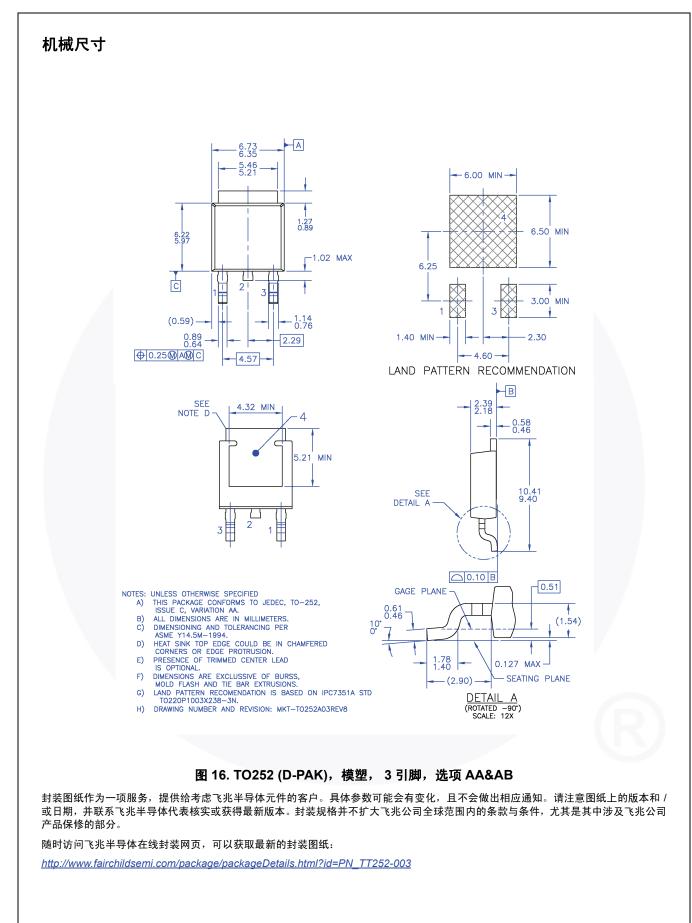
4

 V_{GS} Same Type as DUT Q_g 50K Q 12V 300nF FV_{DS} \mathbf{Q}_{gd} DUT I_G= 常量 Charge 图 12. 栅极电荷测试电路与波形 R VDS V_{DS} 90% 0 V_{DD} V_{GS} R_{G} 10% V_{GS} DUT V_{GS} ∏ a 图 13. 阻性开关测试电路与波形 BV_{DSS} BV_{DSS} - V_{DD} L $E_{AS} = -\frac{1}{2} L I_{AS}^{2} -$ VDS $\mathsf{BV}_{\mathsf{DSS}}$ I_D I_{AS} R_G : V_{DD} I_D (t) |**i**¶ V_{GS} V_{DS} (t) DUT V_{DD} Time t_p 图 14. 非箝位感性开关测试电路与波形

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