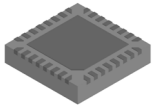


MECHANICAL CASE OUTLINE

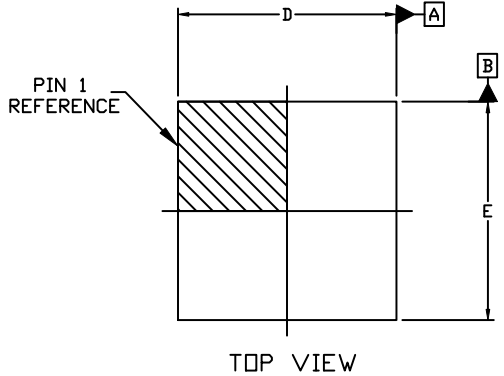
PACKAGE DIMENSIONS

ON Semiconductor®



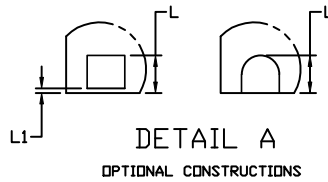
QFN32 4x4, 0.4P
CASE 485GH
ISSUE O

DATE 20 APR 2021

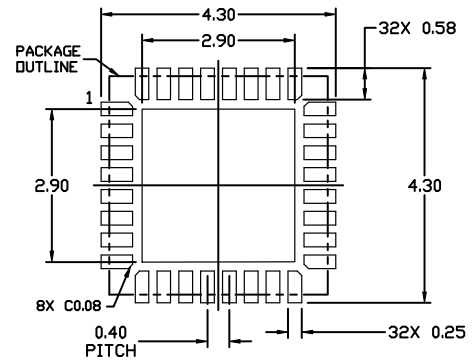
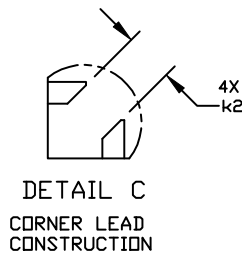
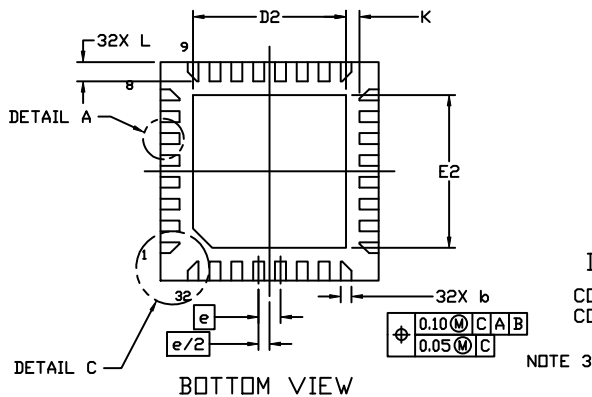
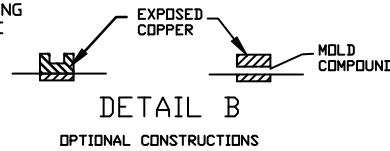
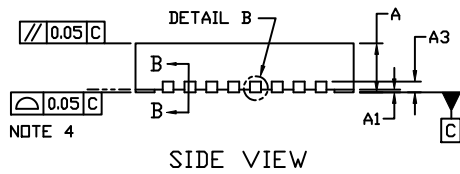


NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS
3. DIMENSION *b* APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30 MM FROM THE TERMINAL TIP.
4. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

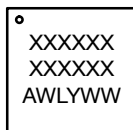


DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.80	0.90	1.00
A1	0.00	---	0.05
A3	0.20 REF		
<i>b</i>	0.15	0.20	0.25
D	3.95	4.00	4.05
D2	2.70	2.80	2.90
E	3.95	4.00	4.05
E2	2.70	2.80	2.90
<i>e</i>	0.40 BSC		
K	0.25 REF		
<i>k</i> 2	0.45 REF		
L	0.25	0.35	0.45
L1	---	---	0.15



RECOMMENDED MOUNTING FOOTPRINT
For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

GENERIC MARKING DIAGRAM*



XXXX = Specific Device Code
A = Assembly Location
WL = Wafer Lot
Y = Year
WW = Work Week

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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